

Grid-on-a-Chip for Accelerating Solar Interconnection

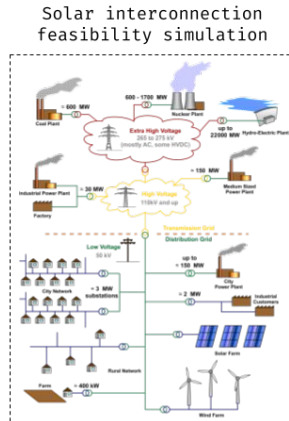
Problem: Technical feasibility studies for grid interconnection are time consuming and lead to costly and unpredictable solar deployment projects.

Solution: *Grid-on-a-Chip* is a grid simulation platform that enables 100X faster feasibility studies for large-scale solar deployments.

Conventional Generator Interconnection Process (GIP) Timeline

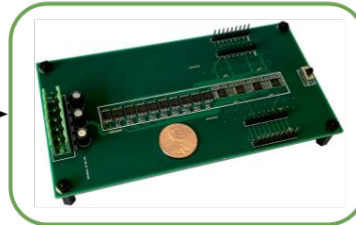


40% Faster GIP Timeline with Vellex Grid-on-a-Chip!



analog scaling

Vellex Grid-on-a-Chip Prototype



Simulation results obtained in near-to-real-time!

Direct analog measurement of grid response



Value Proposition: Time spent on feasibility studies cut in half, 40% faster GIP timeline, up to 25% lower project costs, and less developer uncertainty