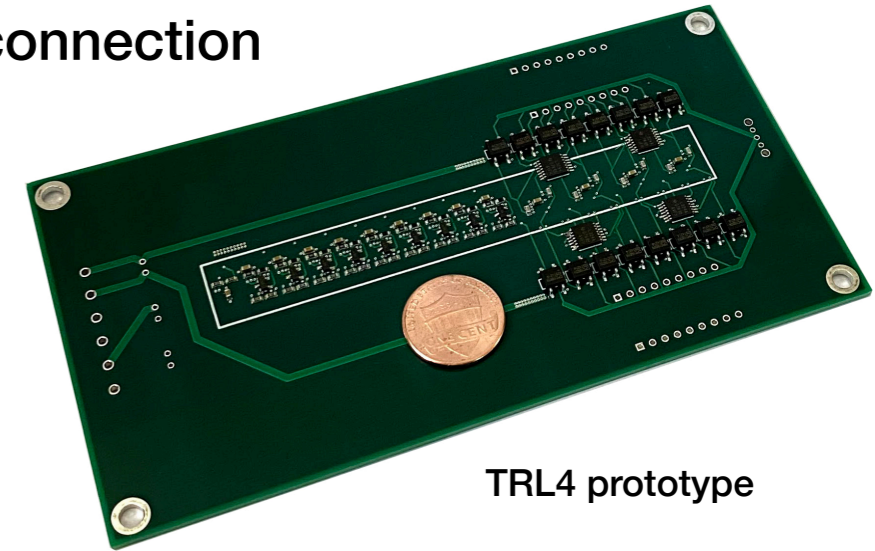


Grid-on-a-chip is a hardware accelerator that conducts solar interconnection studies at least **10,000X** faster than conventional simulators.

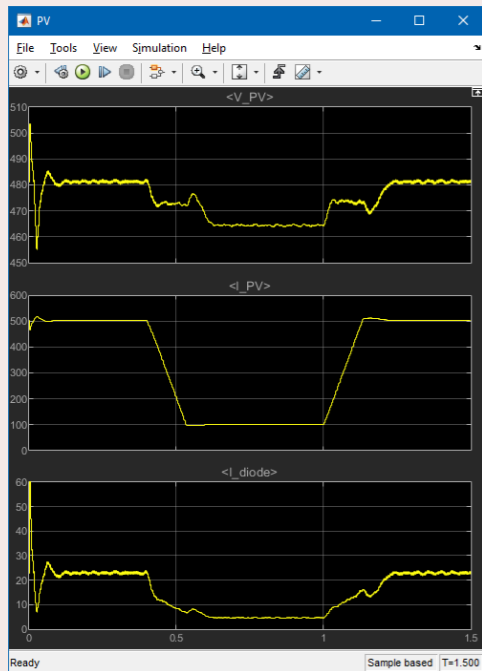


TRL4 prototype

Simulating the interconnection of a 5 MW PV generation system with a 14-bus power system

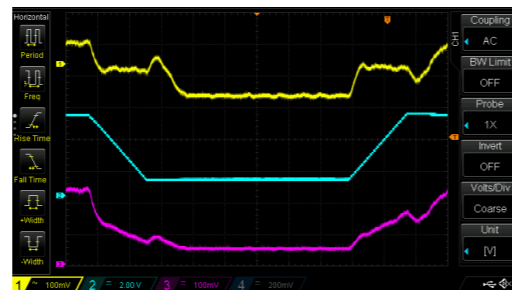
Conventional simulators

1 hour, 51 minutes



VELLEX
COMPUTING
grid-on-a-chip

390 milliseconds



17,000X faster!

Comparable accuracy!

40% faster
deployment timelines

25% lower
balance-of-system costs

About Vellex Computing

- ▶ Founded in 2020 as a Stanford spinout
- ▶ Located in San Luis Obispo, CA — a certified HUBZone company
- ▶ Female CEO and co-founder